

Abstract of the Disclosure

The current in each phase of a multi-phase step motor is monitored and the excess phase current above the set point current, as required on a cycle-by-cycle PWM basis, is

5 reduced thereby bringing each phase current down to the set point. The PWM frequency is automatically adjusted to maintain a minimum number of PWM edges within a portion of the current waveform. A maximum charge / discharge time is specified as a percentage of the PWM period. These provide an accurate independent current control within each phase of a multiphase step motor.

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